

NATIONAL SENIOR CERTIFICATE

GRADE 11

NOVEMBER 2020



ELECTRICAL TECHNOLOGY: DIGITAL ELECTRONICS (EXEMPLAR)

MARKS: 200

TIME: 3 hours



This question paper consists of 11 pages, including a 1-page formula sheet.

INSTRUCTIONS AND INFORMATION

- 1. This question paper consists of NINE questions.
- 2. Sketches and diagrams must be large, neat and fully labelled.
- 3. Show ALL calculations and round off answers to TWO decimal places. Show the units for ALL answers and calculations.
- 4. Number the answers correctly according to the numbering system used in this question paper.
- 5. You may use a non-programmable calculator.
- 6. A formula sheet is provided at the end of this question paper.
- 7. Write neatly and legibly.



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QUESTION 1: OCCUPATIONAL HEALTH AND SAFETY

1.1	Name TWO instances where the user is not required to supply an earth to roofs, gutters, downpipes and wastepipes, on a premises to which electrical energy is supplied.	(2)
1.2	Explain how the following environmental factors could impact negatively on a worker in the workshop:	
	1.2.1 Lack of space	(1)
	1.2.2 Lighting	(1)
1.3	Describe the term <i>anthropometrics</i> .	(2) [6]
QUES	STION 2: TOOLS AND MEASURING INSTRUMENTS	
2.1	What is the purpose of a crimping lug?	(1)
2.2	Explain the advantage of a clamp meter over a digital multimeter when measuring current.	(2)
2.3	Why is it important to stand aside to allow the grinder wheel to run up to full speed before using it?	(2)
2.4	Explain the purpose of a time-base generator in an oscilloscope.	(1) [6]

QUESTION 3: LOGICS

3.1 Refer to FIGURE 3.1 below and answer the questions that follow.

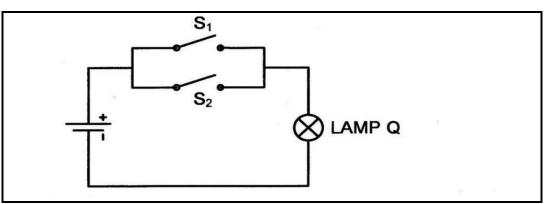
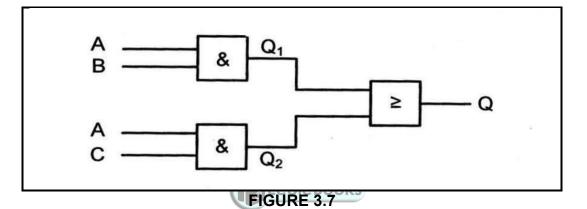


FIGURE 3.1

3.1.1	Identify the logic function of the circuit.	(1)
3.1.2	Draw the logic symbol that is represented by the circuit.	(2)
3.1.3	Draw the truth table of the gate.	(4)
3.1.4	Write the Boolean expression for FIGURE 3.1.	(2)

4	ELECTRICAL TECHNOLOGY: DIGITAL ELECTRONICS	(EC/NOVEMBER 2020)
3.2	Using Boolean algebra, simplify the expression below:	
	$Q = \overline{ABC} + AB\overline{C} + A\overline{B}\overline{C} + \overline{AB}\overline{C}$	(7)
3.3	Use a Karnaugh map to simplify the expression below:	
	$Q = \overline{ABC} + AB\overline{C} + A\overline{B}\overline{C} + \overline{AB}\overline{C}$	(7)
3.4	Name TWO different states a logic probe can operate in.	(2)
3.5	State TWO disadvantages of TTL.	(2)

- 3.6 State TWO disadvantages of CMOS.
- 3.7 Refer to FIGURE 3.7 below and answer the questions that follow.



Give the Boolean expression at the following points:

		[40]
Draw	he truth table for a Half Adder.	(4)
3.7.3	Q	(3)
3.7.2	Q2	(2)
3.7.1	Q ₁	(2)

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3.8

(2)

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<u>(EC/NC</u>	DVEMBER 2020) ELECTRICAL TECHNOLOGY: DIGITAL ELECTRONICS	5
QUE	STION 4: COMMUNICATION SYSTEMS	
4.1	Explain the term <i>resonance</i> .	(2)
4.2	Name THREE types of oscillators	(3)
4.3	Explain the purpose of the Wien bridge oscillator.	(6)
4.4	Explain the purpose of a variable frequency oscillator.	(3)
4.5	Name the applications of a continuous wave transmitter.	(2)
4.6	Explain the term <i>modulation</i> .	(2)
4.7	Draw a block diagram of an AM receiver.	(6)
4.8	Describe the purpose of frequency shift keying.	(2) [26]

QUESTION 5: RLC-CIRCUITS

5.1	Mention ONE factor that directly affects the capacitive reactance of an AC	
	circuit with RC components.	(1)

- 5.2 Draw a neatly labelled graph showing the relationship between the inductive reactance and the frequency in an RLC series circuit. (3)
- 5.3 Study FIGURE 5.3 below and answer the questions that follow.

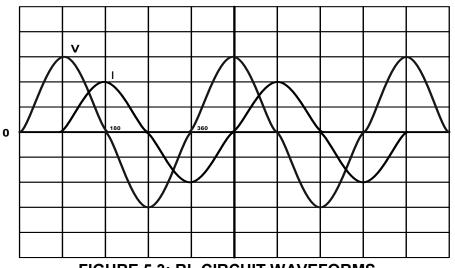
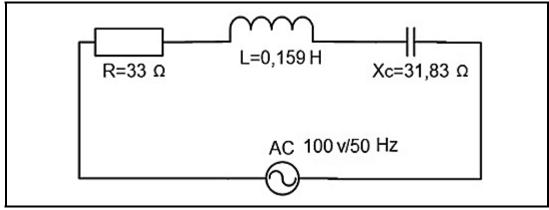


FIGURE 5.3: RL CIRCUIT WAVEFORMS

- 5.3.1 Describe the relationship between the voltage and current waveforms. (1)
- 5.3.2 Explain how an increase in frequency would affect the current waveform.

(3)

5.4 Refer to the circuit diagram in FIGURE 5.4 and answer the questions that follow.





Given: $R = 33 \Omega$ L = 0,159 H $X_C = 31,83 \Omega$ V = 100 Vf = 50 Hz

Calculate:

5.4.1	The inductive reactance of the coil	(3)
5.4.2	The total impedance of the circuit	(3)
5.4.3	The current flowing through the circuit	(3)
5.4.4	The value of the capacitor in the circuit	(3)
	a series circuit with a 600 Ω resistor, an inductive reactance of) and a capacitive reactance of 665 Ω . Describe what occurs to the	

5.5 Given a series circuit with a 600 Ω resistor, an inductive reactance of 37,7 Ω and a capacitive reactance of 665 Ω . Describe what occurs to the impedance of a series circuit when it reaches the point of resonance. (4) [24]

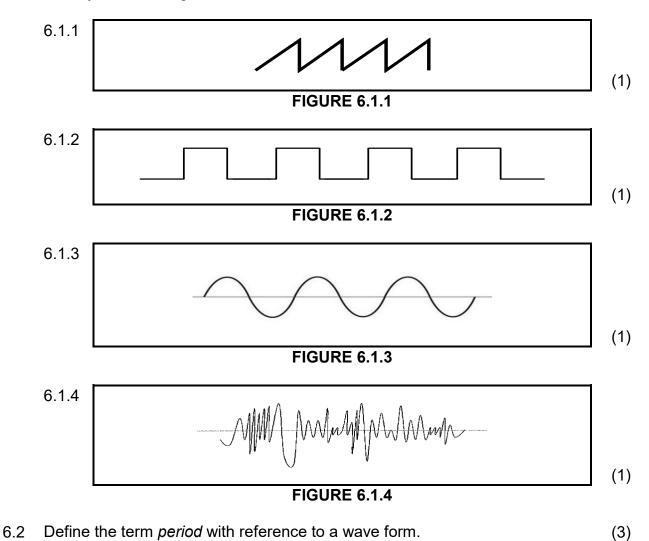
6

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QUESTION 6: WAVEFORMS

6.1 Identify the following waveforms as shown in FIGURES 6.1.1 to 6.1.4.



6.3 For a digital pulse waveform, explain the following terms:

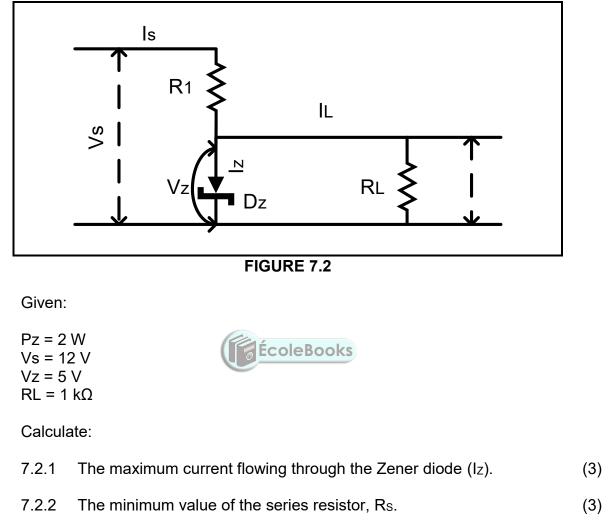
6.7	Mention THREE applications of a radio wave.	(3) [26]
6.6	Describe the concept of <i>clamping</i> in electronics.	(4)
6.5	Determine the periodic time of a wave with a frequency of 500 Hz.	(3)
6.4	An AC supply has an rms voltage of 9 V. Determine its peak voltage.	(3)
	6.3.2 Fall time	(3)
	6.3.1 Pulse width	(3)

QUESTION 7: POWER SUPPLIES

7.1 Draw the block diagram of the series voltage regulator.

(3)

7.2 Refer to the circuit diagram in FIGURE 7.2 below and answer the questions that follow.



7.2.3 The load current IL if a load resistor of 1 kΩ is connected across the Zener diode.
 (3) [12]

QUESTION 8: SEMICONDUCTOR DEVICES

8.1	Describe the term <i>semiconductor</i> .	(3)
8.2	What is the Q-point of a diode?	(3)
8.3	Briefly explain the term majority carriers in a P-type silicon semiconductor.	(2)
8.4	Draw a fully labelled characteristic curve of a TRIAC.	(4)
8.5	Semiconductors are mass produced and are often small in physical size. Manufacturers supply component data sheets. Answer the following questions with reference to component data sheets.	
	8.5.1 State ONE source where such data sheets may be found.	(1)
	8.5.2 Working temperature may be displayed on the sheet. Explain why this information is important.	(3)
	8.5.3 Other than working temperature, state TWO types of information given on data sheets.	(2)
8.6	Draw fully labelled circuit symbols of the following:	
	8.6.1 SCR	(3)
	8.6.2 TRIAC	(3)
8.7	Explain the difference between <i>conventional</i> current flow and <i>electron</i> flow.	(4)
8.8	Describe the term <i>solid state</i> , with reference to semiconductors.	(2)
8.9	Describe how N-type material is formed.	(5)
8.10	Explain how a Zener diode differs from other diodes.	(3)
8.11	For the normal operation of a transistor as a switch, which junction should always be:	
	8.11.1 Forward biased?	(2)
	8.11.2 Reverse biased?	(2)
8.12	Briefly explain TWO ways of switching on the SCR.	(4)
8.13	Name any TWO impurities which are added to pure silicon to create P-type material.	(2) [48]

QUESTION 9: SENSORS AND TRANSDUCERS

		OTAL:	200
9.4	Explain the principle of operation of a Light Dependant Resistor (LD	R).	(4) [12]
9.3	List TWO types of humidity sensors.		(2)
9.2	Describe the basic operation of a dynamic microphone.		(4)
9.1	Define the term ' <i>sensor</i> ' with reference to sensors and transducers.		(2)



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FORMULA SHEET			
WAVE FORMS	RLC CIRCUITS		
Frequency	Inductive reactance		
$f = \frac{1}{T}$	$X_L = 2\pi F l$		
Maximum value	Capacitive reactance		
$V_{MAX=} V_{RMS} \times 1,414 (V)$	$X_C = \frac{1}{2\pi fc}$		
RMS Value	Impendence		
$V_{RMS} = V_{MAX} \times 0,707$	$z = \sqrt{R^2 + (X_L - X_C)^2}$		
Average value	Power factor		
$V_{ave} = V_{max} \times 0,637$	$COS \ \theta = \frac{R}{Z}$		
	$COS \ \theta = \frac{VR}{VZ}$		
POWER SUPPLIES	AMPLIFIERS		
$Vave = Vpk - \frac{1}{2} V_{RIP P-P}$	$V_{CE max} = V_{VCC}$ $V_{CC} = V_{CE} + I_C R_C$		
$V_{OUT} = V_Z$	$I_{C} = \beta I_{B}$		
$Vo = V_Z - V_{BE}$			
$I_L = I_E (\beta + 1) I_B$	$A_V = \frac{Output \ voltage}{input \ voltage}$		
	$A_I = \frac{output\ current}{input\ current}$		





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ELECTRICAL TECHNOLOGY: DIGITAL ELECTRONICS MARKING GUIDELINE (EXEMPLAR)

MARKS: 200

This marking guideline consists of 13 pages.

INSTRUCTIONS TO MARKERS

- 1. All questions with multiple answers imply that any relevant, acceptable answer should be considered.
- 2. Calculations:
 - 2.1 All calculations must show the formulae.
 - 2.2 Substitution of values must be done correctly.
 - 2.3 All answers MUST contain the correct unit to be considered.
 - 2.4 Alternative methods must be considered, provided that the correct answer is obtained.
 - 2.5 Where an incorrect answer could be carried over to the next step, the first answer will be deemed incorrect. However, should the incorrect answer be carried over correctly, the marker has to re-calculate the values using the incorrect answer from the first calculation. If correctly used, the candidate should receive the full marks for subsequent calculations.
 - 2.6 Markers should consider that candidates' answers may deviate slightly from the marking guideline depending on how and where in the calculation rounding off was used.
- 3. These marking guidelines are only a guide with model answers.
- 4. Alternative interpretations must be considered and marked on merit. However, this principle should be applied consistently throughout the marking session.

(EC/NOVEMBER 2020) ELECTRICAL TECHNOLOGY: DIGITAL ELECTRONICS 3

QUESTION 1: OCCUPATIONAL HEALTH AND SAFETY

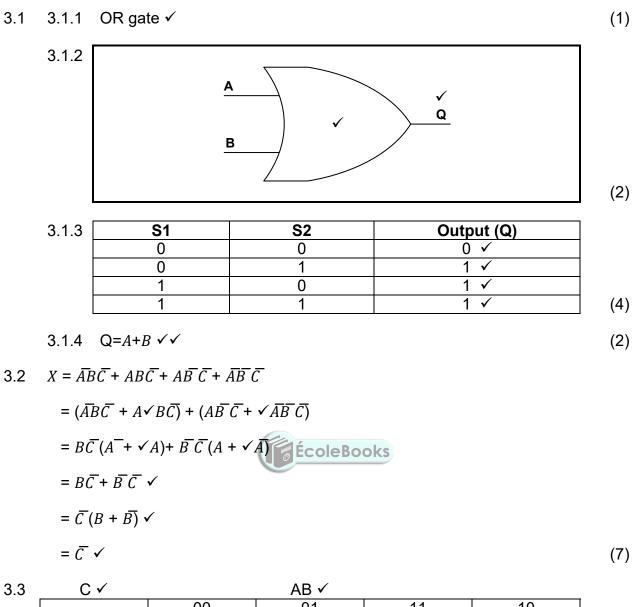
1.1	 Where the operating voltage does not exceed 50 V. ✓ Roofs, gutters, downpipes, and wastepipes made of non-conductive material. On premises which receive electricity by means of underground service connections. ✓ All metallic parts that are not part of the electrical circuit, that can become live, but have an insulated covering. 	(2)	
1.2	1.2.1 Lack of space can lead to chances of mistakes or even injury. \checkmark	(1)	
	1.2.2 Incorrect lighting can lead to eye strain. \checkmark	(1)	
1.3	It is the study of the human body \checkmark and its movement. \checkmark	(2) [6]	
QUESTION 2: TOOLS AND MEASURING INSTRUMENTS			
2.1	A crimping lug offers a quick and permanent solution of terminating a cable. \checkmark	(1)	

2.2	The clamp meter is safer and easier to use, \checkmark because there is no need	
	to connect to the circuit to make measurements. \checkmark	(2)

2.3	This is the time the bonding of the wheel is liable to disengage and break apart. \checkmark Therefore, it is not safe to be standing in the direct path of any pieces that may be thrown out by centrifugal force. \checkmark	(2)
2.4	The time base generator generates the internal saw tooth waveform to control the horizontal sweep of the trace. \checkmark	(1)

(1) [6]

QUESTION 3: LOGICS



		00	01	11	10	
	0	1 ✓	1 ✓	1 ✓	1 ✓	
	1 ✓					(7)
3.4	Logic high ✓ Logic low ✓					(2)
3.5	constructed us	atible with CMC sing discrete co ow current drair	mponents. 🗸	l voltage is the s	same, can be	(2)

Slow switching speed ✓ 3.6 They can be easily destroyed by static electricity \checkmark (2)

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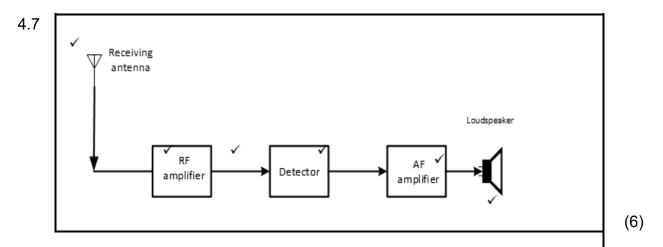
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<u>(EC/NC</u>	OVEMBER 2	020)	ELECTRICAL TEC	HNOLOG	Y: DIGITA		CTRONICS	5
3.7	3.7.1	Q1 ✓ :	= A.B ✓					(2)
	3.7.2	Q2 = A	A √.C √					(2)
	3.7.3	Q = (A	B) ✓ + ✓ (A.C)	√				(3)
			/ (- /					(-)
3.8			Inputs			(Outputs	
		A	В			Sum	Carr	у
		0	0			0	0 √	
		0	1			1	0 √	
		1	0			1	0 ✓	
		1	1			0	1 √	(4)
	-		÷				·	[40]

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QUESTION 4: COMMUNICATION SYSTEMS

- 4.1 Resonance is the increase in amplitude of an oscillation in mechanical or electrical systems, ✓ under the influence of an external periodic impulse of similar frequency to the original vibration. ✓ (2)
- 4.2 The basic LC resonant oscillator ✓
 - Hartley oscillator ✓
 - Colpitts oscillator ✓
 - The RC phase shift oscillator
 - The Wien bridge oscillator
- 4.3 The Wien bridge oscillator is among the simplest sine wave oscillators which uses an RC network, ✓ rather than a tuned LC tank circuit. ✓ It is based on the frequency selective form of a wheatstone bridge circuit. ✓ It uses feedback from two RC circuits, a series RC circuit connected with a parallel RC which together are very frequency selective. ✓ They combine to cause a phase shift of exactly 0° for only the chosen resonant frequencies' ✓ while all higher or lower frequencies are shifted in phase resulting in them not being able to drive the circuit into oscillation. ✓ (6)
- 4.4 A variable frequency oscillator is an oscillator circuit which has one of its oscillating components that is adjustable. ✓ It is a widely used component in all tunable radio receivers and transmitters ✓ that work using the superheterodyne principle. ✓
- 4.5 Correction of radio controlled communication ✓
 Radio control ✓
- 4.6 Modulation refers to the changing of high frequency signal \checkmark in a way that is able to carry information \checkmark



4.8 Frequency-shift keying is a method of being able to transmit a digital pulse signal ✓ using traditional radio transmitting and receiving methods. ✓

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(2) [**26]**

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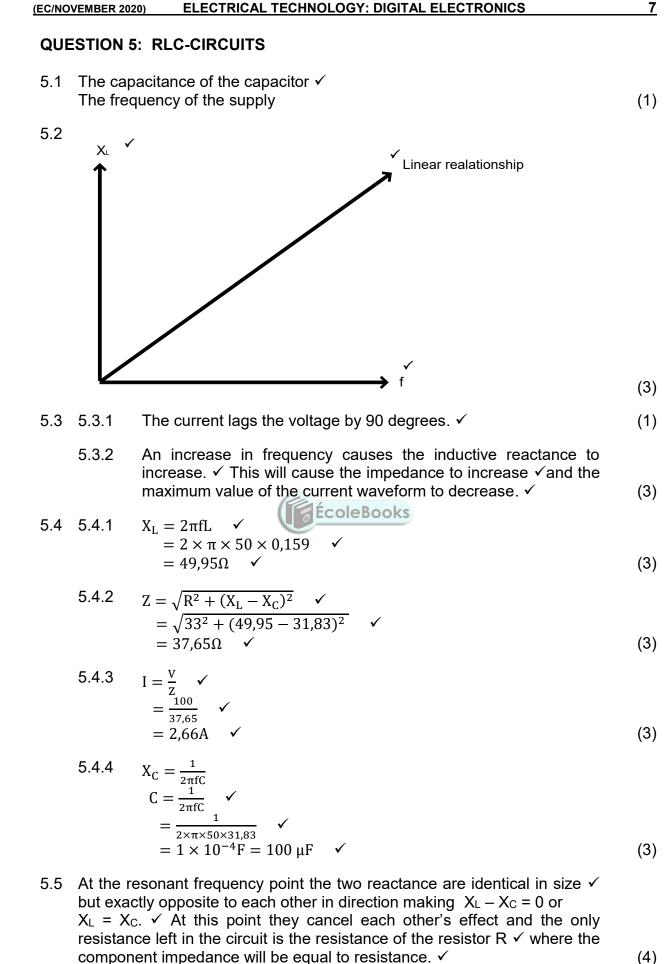
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(2)

(2)

(3)

(3)



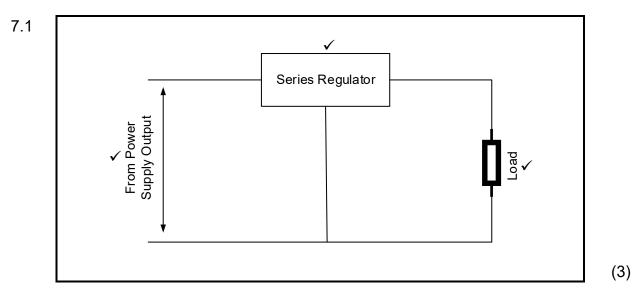
(4) [**24**]

QUESTION 6: WAVEFORMS

6.1	6.1.1	Saw tooth wave ✓	(1)
	6.1.2	Square wave ✓	(1)
	6.1.3	Sine wave ✓	(1)
	6.1.4	Audio wave ✓	(1)
6.2	The pe	eriod is the time taken \checkmark to complete one \checkmark full cycle. \checkmark	(3)
	6.3.1	This is the time between the 50% \checkmark amplitude points on both the rising \checkmark and the falling edges of the pulse. \checkmark	(3)
	6.3.2	Fall time, this is the time a falling pulse takes to make a change from the higher state 'on' \checkmark to the lower state 'off'. \checkmark It is measured between the 10% and 90% points of the completed pulse. \checkmark	(3)
6.4	$V_{rms} =$	$V_{pk} \times 0.707 V$	
	$V_{pk} = $	$\frac{V_{rms}}{0,707} V \checkmark$	
6.5	$= \frac{1}{6}$ $= 1$ $T = \frac{1}{f}$ $= \frac{1}{50}$	$\frac{V_{rms}}{0,707} V \checkmark$ $\frac{9}{0,707} V \checkmark$ $12,73 V \checkmark$ $s \checkmark$	(3)
	$=\frac{1}{50}$	$\overline{0}$ s \checkmark	
		02 sec or 2 ms ✓	(3)
6.6	wavefo circuits	amping circuit actually binds the upper or lower \checkmark extremes of a fixed DC voltage level. \checkmark When unbiased, clamping will fix \checkmark the voltage lower limit \checkmark (or upper limit, in the case of ve clampers) to 0 volt.	(4)
6.7	Ū	nmunication ✓	(-)
0.7	• Bro	adcasting ✓ nputer network ✓	(3) [26]

ELECTRICAL TECHNOLOGY: DIGITAL ELECTRONICS (EC/NOVEMBER 2020)

QUESTION 7: POWER SUPPLIES



7.2 7.2.1
$$P_{Z} = V_{Z} \times I_{Z}$$

$$I_{Z} = \frac{P_{Z}}{V_{Z}} A \checkmark$$

$$= \frac{2}{5} A \checkmark$$

$$= 400 \text{ mA} \checkmark$$
(3)
7.2.2
$$R_{S} = \frac{V_{S} - V_{Z}}{I_{Z}} \Omega \checkmark$$

$$= \frac{12 - 5}{0.4} \Omega \checkmark$$

$$= 17,5 \Omega \checkmark$$
(3)
7.2.3
$$I_{L} = \frac{V_{Z}}{I_{Z}} A \checkmark$$

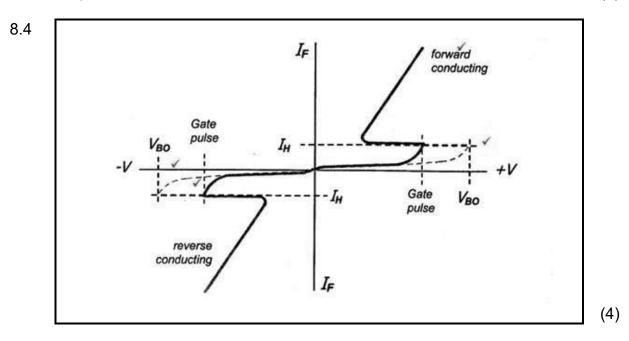
$$I_{L} = \frac{1}{R_{L}} A \checkmark$$

$$= \frac{5}{1000} A \checkmark$$

$$= 5 mA \checkmark$$
(3)
[12]

QUESTION 8: SEMI-CONDUCTOR DEVICES

- 8.1 A semiconductor is a material of which the conductivity ✓ lies between that of a conductor ✓ and an insulator. ✓ Semiconductor devices are electronic components that are made from materials like silicon that have four valance electrons and their conduction can be controlled.
- 8.2 It is the point of intersection ✓ between the diode's characteristic ✓ and the circuits' load line. ✓
- 8.3 The majority carriers in P-type silicon are holes \checkmark formed when adding impurities. \checkmark



8.5.1 •	Internet ✓
---------	------------

•	Manufacturers' technical support material	(1)
---	-------------------------------------------	-----

8.5.2 Semiconductors are very sensitive to temperature. \checkmark It is crucial to know the operating temperature of the device, \checkmark so that it is not destroyed or its operating conditions changed. \checkmark (3)

8.5.3 •	Electrica	al characteristics 🗸

Equivalent values ✓

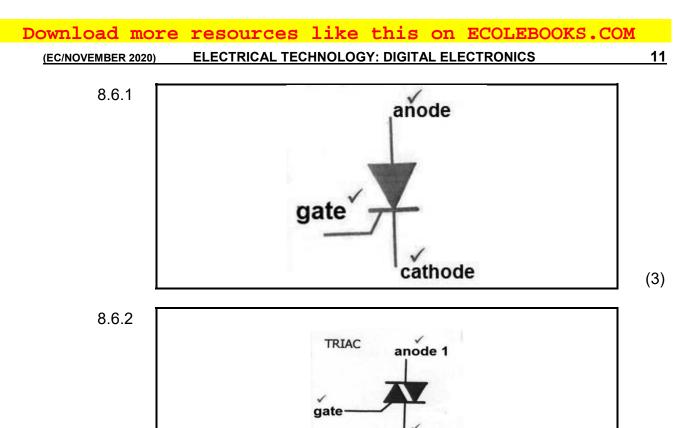
(2)

(3)

(3)

(2)

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- (3)
- 8.7 Electron flow is the movement of electrons \checkmark through material, from a negative potential to a positive potential. \checkmark Conventional current flow is the flow of current ✓ from a positive potential to a negative potential. \checkmark (4) Solid-state devices are devices that are built entirely from solid 8.8 materials \checkmark and in which the electrons or other charge carriers are confined entirely within the solid material. \checkmark (2) 8.9 N-type material is formed when a semiconductor (silicon), which has four valence electrons, \checkmark is doped with a material that has five valence electrons. ✓ Four valance electrons from the semiconductor and from the impurity combine and form covalent bonds. \checkmark The fifth electron remains unbonded. \checkmark This creates an excess of electrons that can be broken away from their atoms and become part of conduction. \checkmark (5) 8.10 A zener diode has a unique reverse biased operating characteristic \checkmark in that it blocks any flow of current when under low reverse voltage \checkmark but as soon as the voltage rises to reach its 'zener breakdown', it breaks down and allows a current to flow in the reverse direction without any damage to itself. ✓ (3)Emitter \checkmark base \checkmark – junction needs to be forward biased. 8.11.1 (2)

anode 2

8.11.2 Collector \checkmark base \checkmark – junction should be reverse biased. (2)

- 8.12 The usual method of turning an SCR on is by forward biasing the anodecathode ✓ terminals and applying a positive voltage to the gate terminal, ✓ by raising the anode-cathode forward biasing voltage ✓ to a large positive level which will force the one reverse biased PN junction to break down. ✓
- 8.13 Boron ✓

12

- Gallium ✓
- Indium

(2) **[48]**

(4)



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QUE	STION 9: SEM	ISORS AND TRA	NSDUC	ERS			
9.1		device that detec it. ✓ It also involv					(2)
9.2	diaphragm pla The sound wa diaphragm vil	microphone has a aced in a magneti aves from a voice orate. ✓ diaphragm attach	ic field cro create a	eated by a j ir pressure	permanent r variations th	magnet. ✓ nat make the	
		ound energy will b	e conver	ted to electr	ical energy.	\checkmark	(4)
9.3	Resistive h	humidity sensor numidity sensor ✓ onductivity sensor	<i>,</i>				(2)
9.4	decrease. ✓	the surface of the					
	If the light on increase. ✓	the surface of the	EDR de	creases, ✓	the resistan	ce will	(4) [12]
						TOTAL:	200















