1: 27-29 Jan (3)	2: 01-05 Feb (5)	3: 08-12 Feb (5)	4: 15-19 Feb (5)	5: 22-26 Feb (5)	6: 1-5 March (5)	7: 8- 12 March (5)	8: 15-19 Mar	9: 23-26 Mar	10: 29-31 March (3)
Occupational Health and Safety	Waveforms	Waveforms	Waveforms	Waveforms	RIC	RIC	RI C	PAT	Revision
1: 27-29 Jan (3) Occupational Health and Safety Occupational Health and Safety Basic introduction to regulations What are regulations? How to use regulations on the workshop Introduction and purpose of the regulations General Machinery Regulations 1988 Supervision of machinery Operation of machinery Working on moving or electrically alive machinery Working on incidents in connection with machinery Devices to start and stop machinery Electrical Machinery Regulations 1988 Safety equipment Electrical Control gear Switchboards Portable electric tools Portable electric tools Conductors Safety Unsafe actions Unsafe conditions Unsafe conditions Dangerous practices Housekeeping principles Signs in the workshop Information signs Fire Safety signs Prohibition signs Fire Safety signs Prohibition signs Fire Safety signs Prohibition signs Fire Safety signs <th>2: 01-05 Feb (5) Waveforms roduction to Waveforms ses of waveforms ifferent types of waves 'aveforms and their applications quare Wave aw tooth Wave iangular Wave ectangular Wave ectangular Wave ectangular Wave adio Wave finition, Symbol & Unit of: ne Sinusoidal Wave stantaneous value aximum value / Minimum value eak to peak value MS value Vrms = 0.707 x Em verage value over half cycle (Vavg = Vmax 637) me period equency uty cycle orm factor oncept of phase and phase difference armonic frequencies (Concept only) ifference between a sound wave and an ctromagnetic wave (Concept y – Self propagating vs. medium needed) ectromagnetic waves (Concept only – nbination of electrical and gnetic wave – unique characteristics) beed of radio waves requency and wavelength monstration: Function Generator and the cilloscope used to measure and splay waveforms</th> <th>3: 08-12 Feb (5) Waveforms Pulse Technique • Pulse polarity • Pulse time • Rise time / Fall time • What is a clock pulse, leading edge, trailing edge? Calculations • Pulse frequency • Rise time • Pall time • Period and frequency • λ (wavelength) & frequency Practical: Set up and measure different waveforms generated by the function generator on the Oscilloscope</th> <th>4: 15-19 Feb (5) Waveforms Wave Shaping Circuits - Diode using discrete components only - Clipping circuits (Positive clipping only) o Simple Series o Series Biased o Simple Parallel o Biased Parallel 6 - Clamping Circuits (Positive clamping only) o Clamping Circuit – Diode o Clamping Circuit – Diode o Clamping Circuit – Zener Diode - Integrator & Differentiator o No calculations o Input and output waveforms on oscilloscope o Construction on breadboard o Measurement of output waveform Practical: Construct each type of clipping and clamping circuit on a breadboard using diodes</th> <th>5: 22-26 Feb (5) Waveforms • Clamping Circuits (Positive clamping only) • Clamping Circuit - Diode • Clamping Circuit - Zener Diode • Integrator & Differentiator • No calculations • Input and output waveforms on oscilloscope • Construction on breadboard • Measurement of output waveform Practical: Construct each type of clipping and clamping circuit on a breadboard using diodes</th> <th>6: 1-5 March (5) RLC Effect of Alternating Current on Resistors, Inductors and Capacitors (RLC) • Components in series circuits only • All applicable calculations relevant to the theory to be completed • Emphasis will be on circuits containing ONE resistor, ONE capacitor and ONE inductor • Wave representation • Phasor diagram • Inductive Reactance o $XL = 2\pi fL$ (Ω) • Capacitive Reactance o $XL = 2\pi fL$ (Ω) • Capacitive Reactance o $XL = 2\pi fL$ (Ω) • Effect of frequency changes on XL and XC Demonstration: Show phase difference between RL and RC</th> <th>7: 8- 12 March (5) RLC • Impedance $oZ = \sqrt{R^2 + (X_L - X_C)^2} (\Omega)$ • Scalar: Representation of the Impedance Triangle • Power o $P = V \times I \cos \theta$ (<i>Watt</i>) • Power Factor O $\cos \theta = \frac{R}{Z}$ O $\cos \theta = \frac{V_R}{V_Z}$ • Phase Angle O $\theta = \cos^{-1} \frac{R}{Z} (Deg)$ O $\theta = \cos^{-1} \frac{V_R}{V_Z} (Deg)$</th> <th>(5) RLC • Natural Resonance • Effect of frequency changes on the impedance and current flow • Resonance with its characteristic curves • Q Factor • Bandwidth • Frequency changes Calculations • Series combination circuits containing ONE resistor, ONE capacitor and ONE inductor • Phasor and wave representation • Resonance • Bandwidth • Q Factor</th> <th>(4) PAT Consolidation</th> <th>March (3) Revision</th>	2: 01-05 Feb (5) Waveforms roduction to Waveforms ses of waveforms ifferent types of waves 'aveforms and their applications quare Wave aw tooth Wave iangular Wave ectangular Wave ectangular Wave ectangular Wave adio Wave finition, Symbol & Unit of: ne Sinusoidal Wave stantaneous value aximum value / Minimum value eak to peak value MS value Vrms = 0.707 x Em verage value over half cycle (Vavg = Vmax 637) me period equency uty cycle orm factor oncept of phase and phase difference armonic frequencies (Concept only) ifference between a sound wave and an ctromagnetic wave (Concept y – Self propagating vs. medium needed) ectromagnetic waves (Concept only – nbination of electrical and gnetic wave – unique characteristics) beed of radio waves requency and wavelength monstration: Function Generator and the cilloscope used to measure and splay waveforms	3: 08-12 Feb (5) Waveforms Pulse Technique • Pulse polarity • Pulse time • Rise time / Fall time • What is a clock pulse, leading edge, trailing edge? Calculations • Pulse frequency • Rise time • Pall time • Period and frequency • λ (wavelength) & frequency Practical: Set up and measure different waveforms generated by the function generator on the Oscilloscope	4: 15-19 Feb (5) Waveforms Wave Shaping Circuits - Diode using discrete components only - Clipping circuits (Positive clipping only) o Simple Series o Series Biased o Simple Parallel o Biased Parallel 6 - Clamping Circuits (Positive clamping only) o Clamping Circuit – Diode o Clamping Circuit – Diode o Clamping Circuit – Zener Diode - Integrator & Differentiator o No calculations o Input and output waveforms on oscilloscope o Construction on breadboard o Measurement of output waveform Practical: Construct each type of clipping and clamping circuit on a breadboard using diodes	5: 22-26 Feb (5) Waveforms • Clamping Circuits (Positive clamping only) • Clamping Circuit - Diode • Clamping Circuit - Zener Diode • Integrator & Differentiator • No calculations • Input and output waveforms on oscilloscope • Construction on breadboard • Measurement of output waveform Practical: Construct each type of clipping and clamping circuit on a breadboard using diodes	6: 1-5 March (5) RLC Effect of Alternating Current on Resistors, Inductors and Capacitors (RLC) • Components in series circuits only • All applicable calculations relevant to the theory to be completed • Emphasis will be on circuits containing ONE resistor, ONE capacitor and ONE inductor • Wave representation • Phasor diagram • Inductive Reactance o $XL = 2\pi fL$ (Ω) • Capacitive Reactance o $XL = 2\pi fL$ (Ω) • Capacitive Reactance o $XL = 2\pi fL$ (Ω) • Effect of frequency changes on XL and XC Demonstration: Show phase difference between RL and RC	7: 8- 12 March (5) RLC • Impedance $oZ = \sqrt{R^2 + (X_L - X_C)^2} (\Omega)$ • Scalar: Representation of the Impedance Triangle • Power o $P = V \times I \cos \theta$ (<i>Watt</i>) • Power Factor O $\cos \theta = \frac{R}{Z}$ O $\cos \theta = \frac{V_R}{V_Z}$ • Phase Angle O $\theta = \cos^{-1} \frac{R}{Z} (Deg)$ O $\theta = \cos^{-1} \frac{V_R}{V_Z} (Deg)$	(5) RLC • Natural Resonance • Effect of frequency changes on the impedance and current flow • Resonance with its characteristic curves • Q Factor • Bandwidth • Frequency changes Calculations • Series combination circuits containing ONE resistor, ONE capacitor and ONE inductor • Phasor and wave representation • Resonance • Bandwidth • Q Factor	(4) PAT Consolidation	March (3) Revision

Coveralls / Ov	eralls									
 Hearing protect 	ction									
Practical: Use	I: Use personal protection									
equipment (Dur	ipment (During practical sessions)									
Chemical Safe	ty (Printed Circuit Board									
manufacturing)										
Revision of Gr	ade 10 PCB methods and									
safety										
Practical: Etcl	h a PCB (Part of PAT									
completion)										
Informal	Classwork / Case studies / Worksheets / Homework / Theory and Practical etc.)									
Assessment:										
Remediation										
				Assian	ment					
	PAT simulation 1 completed									
	The logislation governing workplaces in relation to COVID 19 is the Occupational Health and Safety Act. Act 85 of 1993, as amended, read with the Hazardous Pielosical Agente									
SBA (Formal)	Safety (OHS) Act 85 of 1002									
	Safe work practices are types of administrative controls that include precedures for safe and preper work used to reduce the duration frequency or intensity of expectine to a ba									
	include. Poquiring regular	sale work practices are types of auministrative controls that include procedures for sale and proper work used to reduce the duration, frequency, of intensity of exposure to a had								
	include. Requiring regular	Tand washing of using of aconor-based fidily fut		s should always wa	Si nanus when they a	are visibly solied allu	and removing all			



Regulations. Section 8 (1) of the Occupational Health and

azard. Examples of safe work practices for SARS-CoV-2 ny PPE. Keep safe distances and wear a mask at all times.

TERM 2 (51 days)	1: 13-16 Apr (4)	2: 19-23 Apr (5)	3 : 26-30 Apr (4)	4 : 03-07 May (5)	5 : 10-14 May (5)	6 : 17-21 May (5)	7 : 24-28May (5)	8: 31 May -4 June (5)	9 : 07-11 June	10-11 : 14-25
CAPS Topics	RLC	RLC	Semiconductor Devices	Semiconductor Devices	Semiconductor Devices	Semiconductor Devices	Semiconductor Devices	Semiconductor Devices	PAT Consolidation	Revision
Topics /Concepts, Skills and Values	 Natural Resonance Effect of frequency changes on the impedance and current flow Resonance with its characteristic curves Q Factor Q Factor Bandwidth Frequency changes 	Calculations - Series combination circuits containing ONE resistor, ONE capacitor and ONE inductor - Phasor and wave representation - Resonance - Bandwidth - Q Factor	Introduction to semiconductor devices Component data Where to source data on all types of Electronics components How to read data sheet Pin configuration Typical operating values Working temperature Equivalent components Packages (Dual In Line, TO92, basic packages) Through-hole components vs. surface mount devices Semiconductors Electron flow vs. Conventional flow Semiconductors & solid state Silicon vs. Germanium Doping P & N material Majority carriers/Minority carriers	 PN Diode Construction of a PN Diode Depletion layers Biasing – Forward and reverse Characteristics curve & symbol Calculation of Diode Load Line Practical: Diode Load Line Zener Diode Construction Principle of operation Forward Biasing Avalanche breakthrough vs. controlled breakthrough Zener as a voltage regulator Characteristics curve & symbol Zener calculations Practical: Determine the value of the series resistor for a Zener diode 	 The NPN Transistor Construction Principle of operation Purpose of Biasing & Thermal Runaway Forward Biasing Reverse Biasing Base Curve Emitter Output curve Regions of operations (saturation, active and off) The transistor DC Load Line Transistor power related to the load line (Vcc and Vce) Influence of the DC Load Line on the characteristics of the transistor Symbol Application of Transistors Transistor as a switch Transistor gains Current gain Voltage gain Practical: Built a circuit using the transistor as a switch 	 The PNP Transistor Construction Principle of operation Relation to NPN Symbol Application – simple circuits only Practical: Built a circuit using the transistor as a switch 	 Thyristor - SCR Construction Principle of operation Purpose of Biasing Symbol Characteristics curve Application (Relaxation Oscillator, Phase Control, Switch mode application, DC-DC Converter (buck/boost) Circuit diagram Practical: Construct a Relaxation Oscillator and show waveform on oscilloscope Practical: Construct a light dimmer circuit 	 TRIAC Construction Principle of operation Purpose of Biasing Symbol Characteristics curve Application (Relaxation Oscillator, Phase Control, Switch mode application, DC-DC Converter (buck/boost) Circuit diagram Practical: Construct a light dimmer circuit 		
Resources (other than textbook) to enhance learning										
Informal Assessment: Remediation			Classwork / Cas	e studies / Worksheets / Home	work / Theory and Practical etc.)					
SBA (Formal)	Term Test PAT simulation 2 completed The legislation governing workplaces in relation to COVID – 19 is the Occupational Health and Safety Act, Act 85 of 1993, as amended, read with the Hazardous Biological Agents Regulations. Section 8 (1) of the Occu Act. Act 85 of 1993								ional Health and	Safety (OHS)

TE	RM 3 (52 days)	1: 13-16 Jul (4)	2: 19-23 Jul (5)	3: 26-30 Jul (5)	4: 02-06 Aug (5)	5: 10-13 Aug (4)	6: 16-20 Aug (5)	7: 23-27 Aug (5)	8: 30 Aug- 3 Sep
CAF	S Topics	Semiconductor Devices	Logics	Logics	Logics	Logics	Logics	PAT (project)Consolidation	PAT (project)Consolidat
Top /Cor Skill	cs ncepts, 's and Values	 DIAC Construction Principle of operation Purpose of Biasing Symbol Characteristics curve Application (Relaxation Oscillator, Phase Control, Switch mode application, DC-DC Converter (buck/boost) Circuit diagram application 	 Logic Gate Theory Identify and interpret Logic gates and symbols NOT AND OR/NOR X-OR/X-NOR Apply Logic gates with a maximum of three inputs Truth Table Boolean Expression Following theory, practical combination circuits to be built Converting a Logic Circuit to a Boolean Expression 	 Boolean Algebra Apply commutative and distributive laws Product of sums (POS) Sum of products (SOP) 	 De Morgan's Theorem Combinational/Complex circuits Half and Full Adder Three Input Alarm Complex circuits of choice 	 Karnaugh Maps How to do Karnaugh Map Simplifying Boolean Expressions (Max 4 operands) 	Logic Probe Positive & Negative Logic Active low Active high Practical: Test logic gate outputs using a Logic Probe Resistor Transistor Logic NPN transistor Logic NPN transistor only Input gates only AND, OR and NOT gates in RTL only Practical: Construct RTL logic gates using transistors and resistors (AND, OR and NOT)		
Res than enha	ources (other textbook) to ance learning								
	Informal Assessment: Remediation		Class work/cas	e studies/worksheet	s/homework/ (theory and practi				
Assessment	SBA (Formal)	The legislation gover Safe work practices include. Requiring r	rning workplaces in relatio are types of administrative egular hand washing or us	est tion 3 03, as amended, read Act 85 of 1993, e the duration, freque ash hands when they	d with the Hazardous Biologica ency, or intensity of exposure t are visibly soiled and after rel	I Agents Regulations to a hazard. Example noving any PPE. Kee			

National Revised ATP: Term 3 Grade 11 Electrical Technology:Digital Electronics 2021

pt	9: 06-10 Sept (5)	10-11: 13-23 Sept (9)
tion	Revision	Test
< So	ction 8 (1) of the Oc	cupational Health

ions 0(1)

Imples of safe work practices for SARS-CoV-2 E. Keep safe distances and wear a mask at all

National Revised ATP: Term 4 Grade 11 Electrical Technology: Digital Electronics 2021

INd	Mational Newseu ATT. Term + Grade TT Lieutital Technology. Digital Lieutonius 2021											
Т	ERM 4 (47 days)	1: 05-08 Oct (4)	2: 11-15 Oct (5)	3: 18-22 Oct (5)	4: 25-29 Oct (5)	5: 01-05 Nov (5)	6: 08-12 November (5)	7: 15-19 Nov (5)	8: 22-26 Nov (5)	9: 29 Nov – 3 Dec (5)	10- 06-08 Dec (3)	
СА	PS Topics	Logics	Logics	Sensors and Transducers	Sensors and Transducers	Sensors and Transducers	Sensors and Transducers	PAT Moderation	Test	Test	Test	
Toj Ski	bics /Concepts, Ils and Values	 Transistor Logic Explain why TTL/CMOS logic is used Differences between TTL and CMOS Advantages and disadvantages Application of TTL – no practical circuits of TTL Logic ICs Practical Circuits 40, 70 and 74 series NAND Gate combinational/equivalent circuits NOR Gate combinational/equivalent circuits Practical: Construct logic circuits using Logic ICs 	Transistor Logic Practical: Construct logic circuits using Logic ICs	Introduction to Sensors and Transducers • Definition of sensors and transducers • Piezo Electric Effect • Wheatstone bridge principles of resistance measurement	Functional operation of Sensors and Transducers: Sound • Dynamic Microphone • Electret Microphone • Practical: Connect a microphone to an amplifier and the output of the amplifier to an oscilloscope and display on screen • Light • The LDR • Photodiode • Phototransistor Opto-coupler Practical: Use a Wheatstone bridge with a sensor to show changes in light	 Temperature The Thermistor The Thermistor Thermocouple – working principle and special conditions for use. (Not a linear resistive output – to be used with a lookup table) Practical: Use a Wheatstone bridge with a sensor to show changes in temperature Other types of sensors – application only Gas / Humidity sensor Load cells / Strain sensors Proximity sensors 	Practical: Use a Wheatstone bridge with a sensor to show changes in proximity of metal / humidity					
essmer	Informal Assessment: Remediation		Classwork / Ca	se studies / Workshe	eets / Homework / T	heory and Practical	etc.)					
Asse	SBA (Formal)											